



#### 100V NPN HIGH VOLTAGE TRANSISTOR IN TO252

### **Description**

This Bipolar Junction Transistor (BJT) has been designed to meet the stringent requirements of Automotive Applications.

#### **Features**

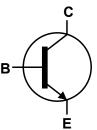
- BV<sub>CEO</sub> > 100V
- I<sub>C</sub> = 3A high Continuous Collector Current
- I<sub>CM</sub> = 5A Peak Pulse Current
- Ideal for Power Switching or Amplification Applications
- Complementary PNP Type: MJD32CQ
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

### **Mechanical Data**

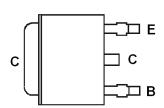
- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 <a>@3</a>
- · Weight: 0.34 grams (approximate)







**Device Schematic** 



Pin Out Configuration Top view

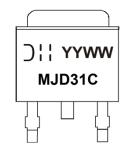
### Ordering Information (Notes 4 & 5)

Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
MJD31CQ-13	Automotive	MJD31C	13	16	2,500

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product\_compliance\_definitions/.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

## **Marking Information**



MJD31C = Product Type Marking Code

O!! = Manufacturers' code marking

YYWW = Date Code Marking

YY = Last Digit of Year (ex: 10 = 2010)

WW = Week Code (01 - 53)



## Absolute Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	100	V
Collector-Emitter Voltage	V <sub>CEO</sub>	100	V
Emitter-Base Voltage	V <sub>EBO</sub>	6	V
Continuous Collector Current	I <sub>C</sub>	3	A
Peak Pulse Collector Current	Ісм	5	A
Continuous Base Current	lв	1	A

# Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
	(Note 6)		3.9		
Power Dissipation	(Note 7)		2.1	W	
Power Dissipation	(Note 8)	P <sub>D</sub>	1.6		
	(Note 9)	1	15		
	(Note 6)		32		
Thermal Resistance, Junction to Ambient Air	(Note 7)	$R_{\theta JA}$	59	°C/W	
	(Note 8)		80		
Thermal Resistance, Junction to Leads	(Note 9)	$R_{ heta JL}$	8.4		
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C		

# ESD Ratings (Note 10)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

- 6. For a device mounted with the exposed collector pad on 50mm x 50mm 2oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.

  7. Same as note (6), except mounted on 25mm x 25mm 1oz copper.

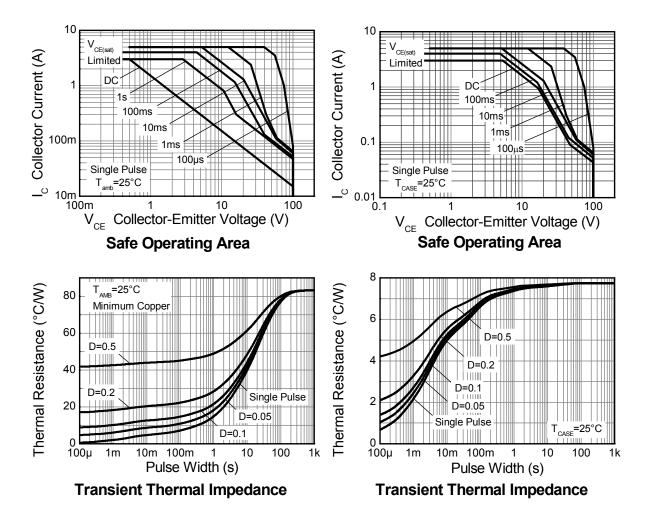
  8. Same as note (6), except mounted on minimum recommended pad (MRP) layout.

  9. Thermal resistance from junction to solder-point (on the exposed collector pad).

  10. Refer to JEDEC specification JESD22-A114 and JESD22-A115.



### **Thermal Characteristics**





## **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Emitter Breakdown Voltage (Note 11)	$BV_{CEO}$	100	_	_	V	$I_C = 30 \text{mA}, I_B = 0$
Collector Cut-off Current	I <sub>CEO</sub>	_	_	1	μΑ	$V_{CB} = 60V, I_B = 0$
Collector Cut-off Current	ICES	_	_	1	μA	V <sub>CE</sub> = 100V, V <sub>EB</sub> = 0
Emitter Cut-off Current	I <sub>EBO</sub>	_	_	1	μΑ	$V_{EB} = 5V, I_{C} = 0$
Collector-Emitter Saturation Voltage (Note 11)	V <sub>CE(sat)</sub>		_	1.2	V	I <sub>C</sub> = 3.0A, I <sub>B</sub> = 375mA
Base-Emitter Turn-On Voltage (Note 11)	V <sub>BE(on)</sub>		_	1.8	V	$I_C = 3A, V_{CE} = 4V$
DC Current Gain (Note 11)	h	25		_		$V_{CE} = 4V, I_{C} = 1A$
DC Current Gain (Note 11)	h <sub>FE</sub>	10		50		$V_{CE} = 4V$ , $I_C = 3A$
Current Signal Current Gain	H <sub>fe</sub>	20	_		_	$V_{CE}$ = 10V, $I_{C}$ = 0.5A, $f$ = 1KHz
Current Gain-Bandwidth Product	f⊤	3.0	_	_	MHz	I <sub>C</sub> = 500mA, V <sub>CE</sub> = 10V, f = 1MHz

Note:

### Typical Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

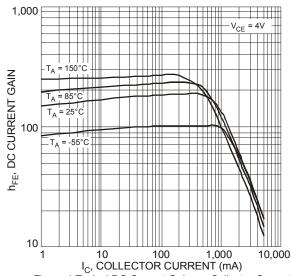
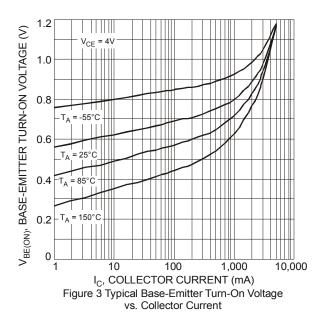
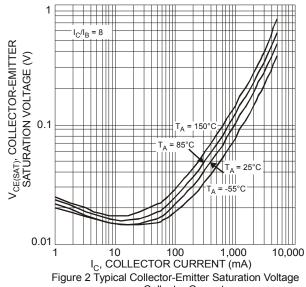


Figure 1 Typical DC Current Gain vs. Collector Current





vs. Collector Current

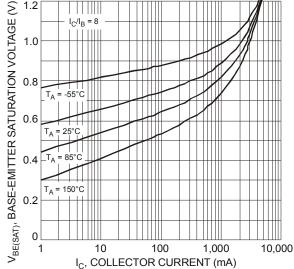
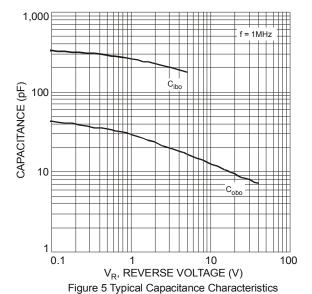


Figure 4 Typical Base-Emitter Saturation Voltage vs. Collector Current

<sup>11.</sup> Measured under pulsed conditions. Pulse width  $\leq 300 \mu s$ . Duty cycle  $\leq 2\%$ .

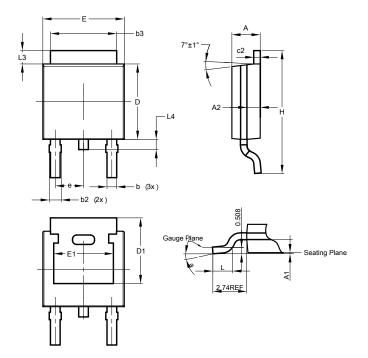






## **Package Outline Dimensions**

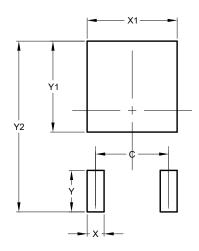
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



TO252					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
A1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
c2	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	-	-		
е	-	_	2.286		
Е	6.45	6.70	6.58		
E1	4.32	_	_		
Н	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	_		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
С	4.572		
X	1.060		
X1	5.632		
Y	2.600		
Y1	5.700		
Y2	10.700		

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device Terminals and PCB tracking.



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